



28118
JA
XA-9796

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Yoshitaka NAKAMURA et al.

Appln. No.: 10/352,133

Group Art Unit: 2811

Filed: January 28, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the time now set for response be extended, retroactively, by three months. If any further extension of time is required to avoid abandonment of this application, such extension is hereby requested. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

08/12/2005 MBEYENE1 00000012 10352133

01 FC:1253

1020.00 OP

MWS:jab

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August 10, 2005

Respectfully submitted,

By: 

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